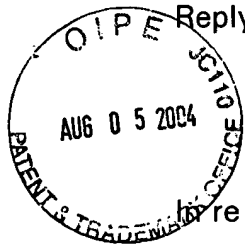


1FW



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Re Application: Jong Hwan KIM

] GRP ART UNIT: 2827

]

Serial No: 10/616,823

] Ex.: Thai, Luan C.

]

Filed: July 10, 2003

]

For: METHOD FOR FORMING BIT LINES OF SEMICONDUCTOR DEVICE

Certification under 37 C.F.R. §1.8(a)

I hereby certify that this paper (along with any paper referred to as being attached or enclosed) is being deposited with The United States Postal Service with sufficient postage as first class mail in an envelope addressed to The Commissioner for Patents, P.O. Box 1450 Alexandria, VA 22313-1450 on August 2, 2004.


W. William Park, Reg. No. 55,523

AMENDMENT UNDER 37 CFR 1.111

The Commissioner for Patents
P.O. Box 1450
Alexandria VA 22313-1450

Sir:

In response to the Office Action dated May 12, 2004, setting a 3-month shortened statutory period for a reply ending on August 12, 2004, Applicant submits the following responsive amendment in the above-identified application. This amendment is considered to place the application in better condition for allowance.

No fee is believed to be required with this amendment, but, if this is not the case, please charge the requisite fee (or credit any overpayment) to Deposit Account No. 12-0400.

Amendments to the Specification begin on page 3 of this paper.

Amendments to the Claims are reflected in the listing of claims, which begins on page 6 of this paper.

Amendment to the Drawings begin on page 9 of this paper and include both an attached replacement sheet and an annotated sheet showing changes.

Remarks/Arguments begin on page 10 of this paper.

An **Appendix** including amended drawing figures is attached following the last page of this paper.